

# SiCortex Job Description

**Title:** Hardware Design Engineer

SiCortex is looking for a hardware design engineer to develop the processor boards for our next-generation dense Linux cluster computers. The job entails modifying existing designs to work with our next-generation processor ASIC (currently under development), migrating the designs from DDR2 to DDR3 and from PCI Express 1.0 to PCI Express 2.0, and revising the on-board power distribution scheme as required.

## **Required:**

- BSEE
- At least 10 years experience writing specifications, choosing components, drawing schematics, developing BOMs, analyzing signal integrity and layout issues, and supervising signal integrity and layout service bureaus.
- Experience with lab debuggers, including facility with oscilloscopes and logic analyzers.
- Experience writing and executing design-verification plans.
- Williness to do production-related work between major design projects.

## **Desired:**

- Experience with designs employing soft-start circuits, intermediate bus converters, and point-of-load regulators.
- Experience with DDR/DDR2 power, termination, layout, and signal integrity.
- Experience with AC-coupled 2.5gbps SerDes layout and signal integrity.
- Experience with Cadence Concept and Allegro schematic capture and layout tools.
- Comfortable with UNIX/Linux command-line-oriented applications.